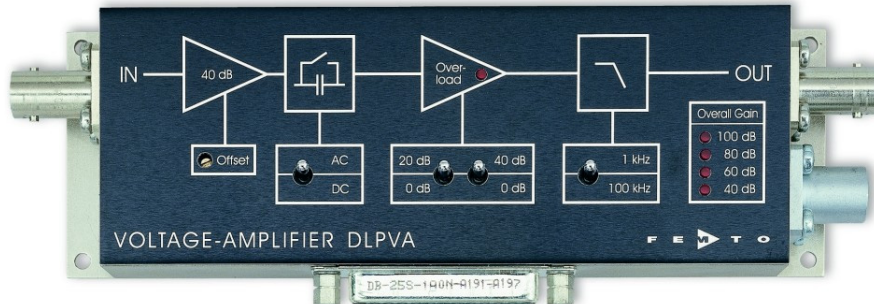




Datasheet

DLPVA-100-BLN-S

Low Noise Variable Gain Low Frequency Voltage Amplifier



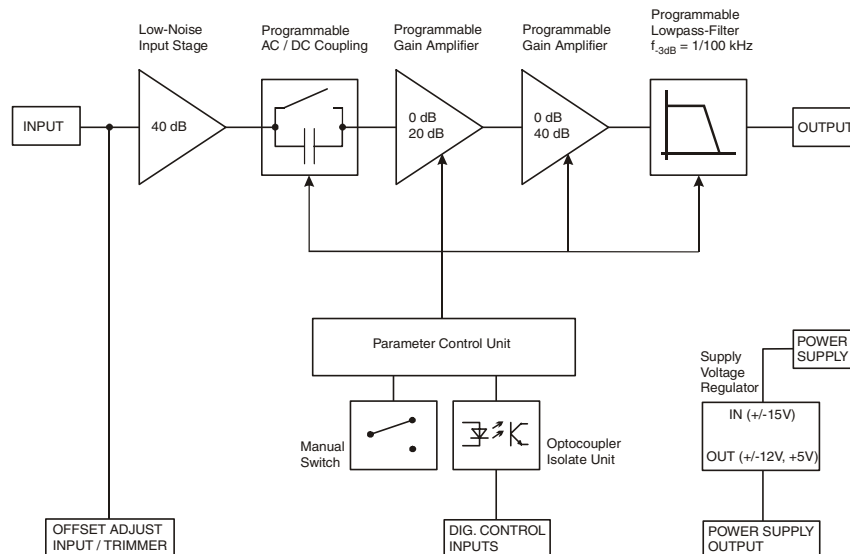
Features

- **Variable Gain 40 to 100 dB, Switchable in 20 dB Steps**
- **Bipolar Input Stage, Recommended for Low Impedance Sources Smaller than 100 Ω**
- **Very Low Input Voltage Noise: 700 pV/√Hz**
- **DC-Coupled, Single Ended**
- **0.5 μV/°C DC-Drift**
- **Bandwidth DC - 100 kHz, Switchable to 1 kHz**
- **Switchable AC/DC-Coupling**
- **Local and Remote Control**

Applications

- **Low-Noise Laboratory Amplifier**
- **Pulsed Thermal EMF Analysis**
- **Industrial Sensors**
- **Detector Preamplifier**
- **Integrated Measurement Systems**

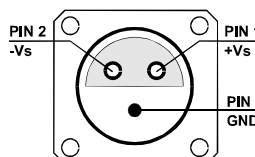
Block Diagram



Low Noise Variable Gain Low Frequency Voltage Amplifier

Specifications	<i>Test Conditions</i>	<i>V_s = ± 15 V, T_a = 25°C</i>	
Gain	Gain Values	40, 60, 80, 100 dB indicated by four LEDs	
	Gain Accuracy	± 0.1 % (between settings) ± 1 % (overall)	
	Gain Flatness	± 0.1 dB	
Frequency Response	Lower Cut-Off Frequency	DC, switchable to 1.5 Hz	
	Upper Cut-Off Frequency	100 kHz, switchable to 1 kHz	
	Upper Cut-Off Frequency Rolloff	12 dB/Oct.	
Time Response	Rise / Fall Time (10% - 90%)	3.5 μs (@ BW = 100 kHz) 350 μs (@ BW = 1 kHz)	
Input	Input Impedance	1 MΩ	
	Input Voltage Drift	0.5 μV/°C	
	Equivalent Input Voltage Noise	<u>Gain Setting</u>	<u>Noise</u>
		100 dB	700 pV/√Hz
		80 dB	730 pV/√Hz
		60 dB	860 pV/√Hz
	40 dB	6 nV/√Hz	
	Equivalent Input Current Noise	3 pA/√Hz	
	1/f-Noise Corner	80 Hz	
	Input Bias Current	1 μA	
Input Bias Current Drift	8 nA/°C		
Input Offset Voltage	± 500 μV, adjustable by offset trimmer and external control voltage		
Output	Output Impedance	50 Ω (terminate with > 10 kΩ for best performance)	
	Output Voltage Range For Linear Amplification	± 10 V (@ > 10 kΩ load)	
	Output Current (max.)	± 20 mA	
	Output Overload Recovery Time	0.5 ms (after 20x overload)	
Overload LED	<p>The amplifier features a LED to signalize an overload condition. The Overload LED will turn on if the signal level within the signal path exceeds the linear operating range. In order to ensure the correct operation of the amplifier without signal distortions reduce the gain setting until the Overload LED turns off.</p> <p>The Overload LED may also turn on when the amplifier is operated with open input or with a high source impedance. For proper operation please use a source impedance of less than 1 kΩ or switch to a lower gain setting.</p>		
Remote Offset Control	Offset Control Voltage Range	± 10 V, corresponds to ± 500 μV input offset	
	Offset Control Input Impedance	200 kΩ	
Remote Digital Control	Control Input Voltage Range	Low: - 0.8 ... + 0.8 V High: + 1.8 ... + 12 V, TTL / CMOS compatible	
	Control Input Current	0 mA @ 0 V, 1.5 mA @ + 5 V, 4.5 mA @ + 12 V	
	Overload Output	Non active: + 5 V, max. 1 mA, active: 0.8 V, max. -10 mA	

Low Noise Variable Gain Low Frequency Voltage Amplifier

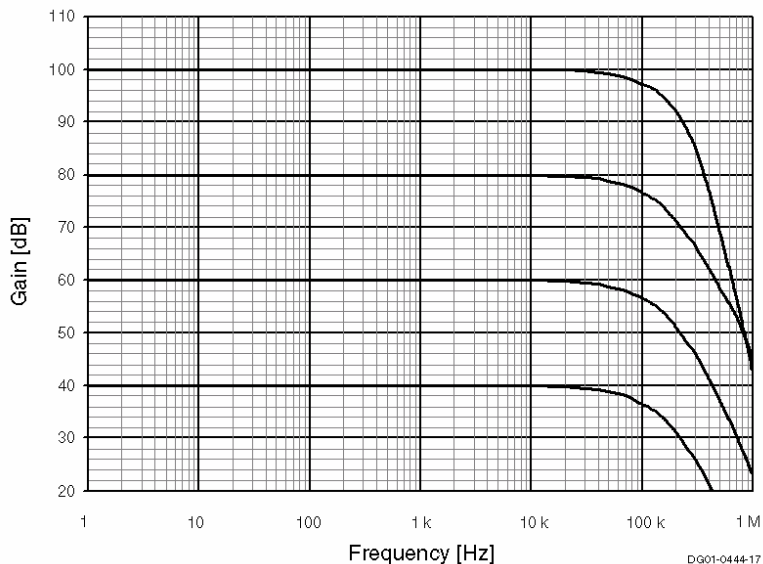
Power Supply	Supply Voltage	± 15 V (± 14.5 V to ± 16 V)
	Supply Current	± 75 mA typ. (depends on operating conditions, recommended power supply capability minimum 200 mA)
Case	Weight	0.32 kg (0.7 lbs)
	Material	AlMg4.5Mn, nickel-plated
Temperature Range	Storage Temperature	- 40 °C to + 100 °C
	Operating Temperature	0 °C to + 60 °C
Absolute Maximum Ratings	Power Supply Voltage	± 21 V
	Control Input Voltage	+ 16 V / - 5 V
	Signal Input Voltage	± 4.5 V
Overvoltage at the signal input can severely degrade the noise performance or destroy the amplifier!		
Connectors	Input	BNC
	Output	BNC
	Power Supply	LEMO series 1S, 3-pin fixed socket Pin 1: + 15V Pin 2: - 15V Pin 3: GND
		
	Control Port	Sub-D 25-pin, female, qual. class 2 Pin 1: +12 V (stabilized power supply output, max. 100 mA) Pin 2: -12 V (stabilized power supply output, max. 100 mA) Pin 3: AGND (analog ground) Pin 4: +5 V (stabilized power supply output, max. 50 mA) Pin 5: digital output: overload Pin 6: NC Pin 7: NC Pin 8: offset control voltage input Pin 9: DGND (ground f. digital control Pin 10 - 25) Pin 10: NC Pin 11: digital control input: gain, LSB Pin 12: digital control input: gain, MSB Pin 13: digital control input: AC/DC Pin 14: digital control input: 100 kHz / 1 kHz Pin 15 - 25: NC

Low Noise Variable Gain Low Frequency Voltage Amplifier

Remote Control Operation	<p>General</p> <p>Remote control input bits are opto-isolated and connected by logical OR to local switch setting. For remote control a switch setting, set the corresponding local switch to "0 dB", "AC" and "1 kHz" and select the wanted setting via a bit-code at the corresponding digital inputs. Mixed operation, e.g. local gain setting and remote controlled bandwidth setting, is also possible.</p>															
Gain Setting	<table style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="border-bottom: 1px solid black;">Gain</th> <th style="border-bottom: 1px solid black;">Pin 11</th> <th style="border-bottom: 1px solid black;">Pin 12</th> </tr> </thead> <tbody> <tr> <td>40 dB</td> <td>low</td> <td>low</td> </tr> <tr> <td>60 dB</td> <td>high</td> <td>low</td> </tr> <tr> <td>80 dB</td> <td>low</td> <td>high</td> </tr> <tr> <td>100 dB</td> <td>high</td> <td>high</td> </tr> </tbody> </table>	Gain	Pin 11	Pin 12	40 dB	low	low	60 dB	high	low	80 dB	low	high	100 dB	high	high
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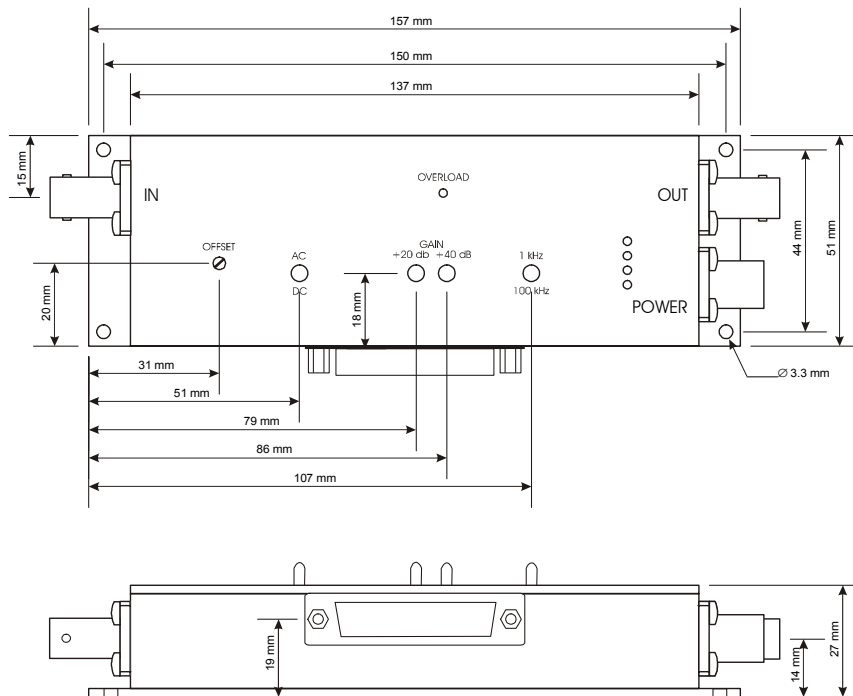
Typical Performance Characteristics

Frequency Response (Logarithmic)



Low Noise Variable Gain Low Frequency Voltage Amplifier

Dimensions



DZ01-0440-18

FEMTO Messtechnik GmbH
 Klosterstr. 64
 D-10179 Berlin · Germany
 Tel.: +49-(0)30-280 4711-0
 Fax: +49-(0)30-280 4711-11
 e-mail: info@femto.de
 http://www.femto.de

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Datasheet

LUCI-10

**USB to D-Sub Control Interface
for FEMTO Amplifiers**



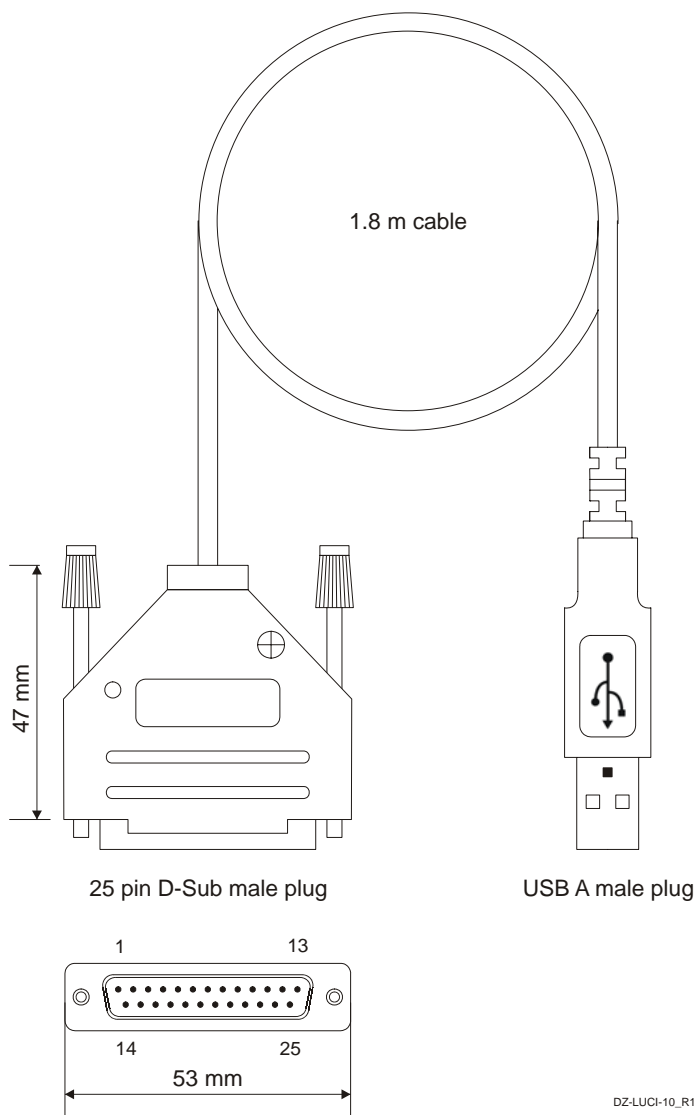
<p>Features</p>	<ul style="list-style-type: none"> • Compact Digital I/O Interface for USB Remote Control of FEMTO Amplifiers • Supports Opto-Isolation of Amplifier Signal Path from PC USB Port • 16 Digital Outputs, 3 Opto-Isolated Digital Inputs • Bus-Powered Operation • System Driver, Application Software and VI's for use with LabVIEW™ Included 																		
<p>Applications</p>	<ul style="list-style-type: none"> • Remote Control of FEMTO® Amplifiers and Photoreceivers Directly from a PC 																		
<p>Block Diagram</p>	<p>The block diagram illustrates the system architecture. On the left, a 'Windows PC' is connected to the 'LUCI-10' device via a 'USB Cable' and a 'USB Type A' connector. The LUCI-10 is '+ 5 V, Bus Powered'. Inside the LUCI-10, a 'Microcontroller' manages a 'USB Controller' and 'Digital I/O'. The 'Digital I/O' block includes a 'Digital Out 16 Bit' and a 'Digital In 3 Bit'. The 'Digital Out' is connected to an 'LED' and an 'Opto-Isolation' block. The 'Digital In' is connected to another 'Opto-Isolation' block. Both opto-isolation blocks are connected to a 'D-Sub 25 Pin Mate' which interfaces with the 'FEMTO Amplifier'. The amplifier provides 'Amplifier Control Bits' and 'Amplifier Status Bits' back to the LUCI-10.</p>																		
<p>Hardware Specifications</p>	<table border="0"> <tr> <td data-bbox="264 1619 475 1650"> <p>General Characteristics</p> </td> <td data-bbox="548 1619 727 1677"> <p>Bus Interface Digital I/O Channels</p> </td> <td data-bbox="857 1619 1092 1703"> <p>USB 2.0 (full-speed) 16 output lines 3 opto-isolated input lines</p> </td> </tr> <tr> <td></td> <td data-bbox="548 1703 610 1734"> <p>Supply</p> </td> <td data-bbox="857 1703 1287 1766"> <p>PC USB port, + 5 V, typ. 100 mA, bus-powered (no auxiliary power supply required)</p> </td> </tr> <tr> <td></td> <td data-bbox="548 1766 651 1797"> <p>Connectors</p> </td> <td data-bbox="857 1766 1040 1824"> <p>USB type A D-Sub, 25 pin, male</p> </td> </tr> <tr> <td></td> <td data-bbox="548 1824 602 1856"> <p>Cable</p> </td> <td data-bbox="857 1824 1060 1856"> <p>AWG 28, length 1.8 m</p> </td> </tr> <tr> <td data-bbox="264 1877 329 1908"> <p>Output</p> </td> <td data-bbox="548 1877 732 1929"> <p>Number of Channels Output Voltage Range</p> </td> <td data-bbox="857 1877 1365 1992"> <p>16 output lines, supporting opto-isolation inside FEMTO amplifiers and photoreceivers LOW bit: 0 ... + 0.5 V (@ 0 ... 2 mA output current) HIGH bit: + 4 ... + 5.5 V (@ 0 ... 2 mA output current)</p> </td> </tr> <tr> <td></td> <td data-bbox="548 1992 667 2045"> <p>Max. Current Writing Rate</p> </td> <td data-bbox="857 1992 1149 2045"> <p>6 mA per channel max. 800 operations per second</p> </td> </tr> </table>	<p>General Characteristics</p>	<p>Bus Interface Digital I/O Channels</p>	<p>USB 2.0 (full-speed) 16 output lines 3 opto-isolated input lines</p>		<p>Supply</p>	<p>PC USB port, + 5 V, typ. 100 mA, bus-powered (no auxiliary power supply required)</p>		<p>Connectors</p>	<p>USB type A D-Sub, 25 pin, male</p>		<p>Cable</p>	<p>AWG 28, length 1.8 m</p>	<p>Output</p>	<p>Number of Channels Output Voltage Range</p>	<p>16 output lines, supporting opto-isolation inside FEMTO amplifiers and photoreceivers LOW bit: 0 ... + 0.5 V (@ 0 ... 2 mA output current) HIGH bit: + 4 ... + 5.5 V (@ 0 ... 2 mA output current)</p>		<p>Max. Current Writing Rate</p>	<p>6 mA per channel max. 800 operations per second</p>
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USB to D-Sub Control Interface for FEMTO Amplifiers

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Optional Requirements	<p>For development of own application programs an additional development environment like LabVIEW Version 8 (or higher) or C/C++ is required.</p>												
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USB to D-Sub Control Interface for FEMTO Amplifiers

Dimensions



FEMTO Messtechnik GmbH
Paul-Lincke-Ufer 34
D-10999 Berlin · Germany
Tel.: +49 (0)30 – 4 46 93 86
Fax: +49 (0)30 – 4 46 93 88
e-mail: info@femto.de
<http://www.femto.de>

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